U.S. DEPARTMENT OF COMMERCE ATTY. DOCKET NO. SERIAL NO. FORM PTO-1449 (REV. 7-80) PATENT AND TRADEMARK OFFICE NIT-202-02 APPLICANT LIST OF DOCUMENTS CITED BY APPLICANT H. MATSUOKA et al (Use several sheets if necessary) **GROUP** FILING DATE 07/31/03 2814 **U.S. PATENT DOCUMENTS** EXAMINER FILING DATE CLASS SUBCLASS DOCUMENT DATE NAME INITIAL (If Appropriate TT 6,130,449 10/2000 Matsuoka et al AΒ 6,100,117 08/2000 Hao et al TT AÇ ΑD AE AF AG AH ΑI AJ ΑK FOREIGN PATENT DOCUMENTS TRANSLATIO DOCUMENT DATE COUNTRY SUBCLASS CLASS NO 11 07-066299 03/10/95 Japan AL AM ΑN AO AP OTHER DOCUMENTS (Including Author, Title, Date, Pertinent Pages, etc.) Ahn et al "Bidirectional Matched Global Bit Line Scheme for High Density AR TT DRAMs", Symposium on VLSI Circuits, 1993, pp. 91-92. AS AT -**EXAMINER** DATE CONSIDERED 05-27-04 * EXAMINER: Initial if reference considered, whether or not citation is in conformance with MPEP 609; Draw line through citation if not in conformance and not considered. Include copy of this form next communication to applicant.